EE 206 Circuit Theory



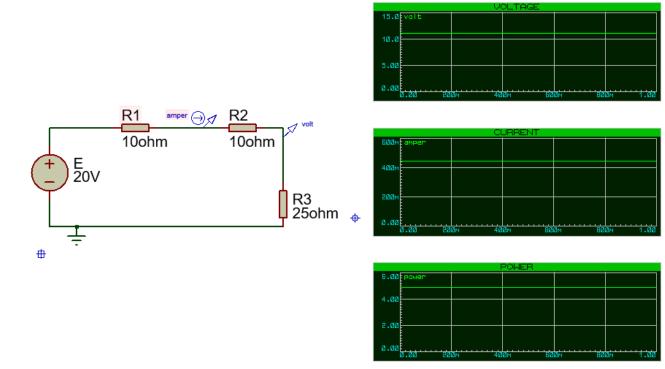


Figure 1

LAB WORK

Implement the circuit of Figure 2 in Proteus. Plot the $V_1,\,I_1\,\text{and}\,I_4$ values.

